



08-11-05

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in

IN THE UNITED STATES PATENT OFFICE

App. No.: 09/800,213 Confirmation No. 4985
Filing Date: March 6, 2001
Applicant: John Howard Coleman
Examiner: Tran, Minh Loan Art Unit 2826

Commissioner for Patents by EXPRESS MAIL on August 10, 2005
P.O. Box 1450 Mailing Label No. EQ 012726798 US
Alexandria, VA 22313-1450

AMENDMENT

Sir:

Please enter this amendment which is in response to the Office Action of January 4, 2005.

The applicant requests a 5-month extension of time for this response. Payment authorization for the small-entity extension fee of \$1080 is attached.

An amendment to the title is shown on page 2 of this paper.

Amendments to the specification are shown in the listing of paragraphs beginning on page 3 of this paper.

Amendments to the claims are shown in the listing of claims beginning on page 7 of this paper.

Remarks/arguments begin on page 9 of this paper.

A formalized replacement drawing is attached to this paper.

08/12/2005 CNGUYEN2 00000051 09800213

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1080.00 DP

Amendment to the Title

[~~SEMICONDUCTOR FABRICATION AND STRUCTURE FOR~~]
FABRICATION METHOD FOR SILICON-ON DEFECT LAYER IN
FIELD-EFFECT AND BIPOLAR TRANSISTOR DEVICES